

Substitute for Form 1449/PTO



**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Sheet	1	of	2	Attorney Docket Number	004640.P019
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U.S. PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
JK		us-	6,237,123	05-22-2001	Ilyoung Kim et al.	
		us-	6,205,564	03-20-2001	Ilyoung Kim et al.	
		us-	6,085,334	07-04-2000	Giles	
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		us-	10/236,248	09-05-2002	Zorian et al.	
		us-				

Examiner Signature	/James Kerveros/ (01/19/2007)	Date Considered	01/19/2007
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Sheet

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of

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<i>Complete if Known</i>	
Application Number	10/664,190
Filing Date	September 16, 2003
First Named Inventor:	Yervant Zorian et al.
Art Unit	2138
Examiner Name	Kerveros, James C.

Attorney Docket Number 004640.P019

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
JK	✓	CHANG, Tsin-Yuan and ZORIAN, Yervant: "Tutorial 2: SoC Testing and P1500 Standard," Asian Test Symposium 2000: 492	
		MARINISSEN, Erik Jan, et. al.: "Wrapper Design for Embedded Core Test," ITC 2000: 911-920.	
	✓	ZORIAN, Yervant and SHOUKOURIAN, Samuel: "Embedded-Memory Test and Repair: Infrastructure IP for SoC Yield," IEEE CS and IEEE CASS May-June 2003: 58-66	
	✓	BENSO, Alfredo, et. al.: "HD2BIST: a Hierarchical Framework for BIST Scheduling, Data Patterns Delivering and Diagnosis in SoCs," ITC 2000: 892-901	
	✓	GIZOPOULOS, Dimitris, et. al.: "Low Power/Energy BIST Scheme for Datapaths," VTS 2000: 23-28	
	✓	VARDANIAN, Valery A. and ZORIAN, Yervant: "A March-based Fault Location Algorithm for Static Random Access Memories," MDTD 2002: 256-261	
↓	✓	BENSO, Alfredo, et. al., "HD-BIST: A Hierarchical Framework for BIST Scheduling and Diagnosis in SoCs," ITC 1999: 1038-1044	

Examiner Signature	/James Kerveros/ (01/19/2007)	Date Considered	01/19/2007
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